



# DFE Commissioning



1. CFT Experts Verification
2. DFE Ware
  - FPD Boards description
  - GUI Interaction – Slow rate monitor (1553), VHDL code
3. Platform download
4. Link Test – VHDL Code
5. Arbiter Verification – VHDL Code
6. Test Vectors from AFE (Personality Code)
7. Test Vectors from DFE – VHDL Code
8. L3 Sender Implementation, Test and Verification (Data Pump)
9. Verification of the Test Vectors - TM side
10. Verification of real data using L3 Sender output VS data stored.

# Time Line

